

22264 U.S. PTO
10/759193

012004

APPENDIX B

BUS SIGNAL ASSIGNMENTS

Table 3: PC/104-Plus Bus Signal Assignments

| J3/P3 | | | | |
|-------|---------------------------|----------|----------|---------------------------|
| Pin | A | B | C | D |
| 1 | GND/5.0V KEY ² | Reserved | | |
| 2 | +5V/I/O | AD02 | AD01 | +5V/I/O |
| 3 | AD05 | GND | AD04 | AD03 |
| 4 | C/BE0* | AD07 | GND | AD06 |
| 5 | GND | AD09 | AD08 | GND |
| 6 | AD11 | +5V/I/O | AD10 | M66EN |
| 7 | AD14 | AD13 | GND | AD12 |
| 8 | +3.3V | C/BE1* | AD15 | +3.3V |
| 9 | SERR* | GND | SB0* | PAR |
| 10 | GND | PERR* | +3.3V | SDONE |
| 11 | STOP* | +3.3V | LOCK* | GND |
| 12 | +3.3V | TRDY* | GND | DEVSEL* |
| 13 | FRAME* | GND | IRDY* | +3.3V |
| 14 | GND | AD16 | +3.3V | C/BE2* |
| 15 | AD18 | +3.3V | AD17 | GND |
| 16 | AD21 | AD20 | GND | AD19 |
| 17 | +3.3V | AD23 | AD22 | +3.3V |
| 18 | IDSEL0 | GND | IDSEL1 | IDSEL2 |
| 19 | AD24 | C/BE3* | +5V/I/O | IDSEL3 |
| 20 | GND | AD26 | AD25 | GND |
| 21 | AD29 | +5V/I/O | AD28 | AD27 |
| 22 | +5V/I/O | AD30 | GND | AD31 |
| 23 | REQ0* | GND | REQ1* | +5V/I/O |
| 24 | GND | REQ2* | +5V/I/O | GNT0* |
| 25 | GNT1* | +5V/I/O | GNT2* | GND |
| 26 | +5V/I/O | CLK0 | GND | CLK1 |
| 27 | CLK2 | +5V/I/O | CLK3 | GND |
| 28 | GND | INTD* | +5V/I/O | RST* |
| 29 | +5V/I2M | INTA* | INTB* | INTC* |
| 30 | +12V | Reserved | Reserved | GND/3.3V KEY ² |

Notes:

1. The shaded area denotes power or ground signals.
2. The KEY pins are to guarantee proper module installation. Pin-A1 will be removed and the female side plugged for 5.0V I/O signals and Pin-D30 will be modified in the same manner for 3.3V I/O. It is recommended that both KEY pins (A1 and D30) be electrically connected to GND for shielding.

Table 4: PC/104 Bus (Reference Only)

| J1/P1 | | |
|-------|----------|----------|
| Pin | Row A | Row B |
| 1 | IOCHCHK* | GND |
| 2 | SD7 | RESETDRV |
| 3 | SD6 | +5V |
| 4 | SD5 | IRQ9 |
| 5 | SD4 | -5V |
| 6 | SD3 | DRQ2 |
| 7 | SD2 | -12V |
| 8 | SD1 | ENDXFR* |
| 9 | SD0 | +12V |
| 10 | IOCHRDY | KEY |
| 11 | AEN | SMEMW* |
| 12 | SA19 | SMEMR* |
| 13 | SA18 | IOW* |
| 14 | SA17 | IOR* |
| 15 | SA16 | DACK3* |
| 16 | SA15 | DRQ3 |
| 17 | SA14 | DACK1* |
| 18 | SA13 | DRQ1 |
| 19 | SA12 | REFRESH* |
| 20 | SA11 | SYSCLK |
| 21 | SA10 | IRQ7 |
| 22 | SA9 | IRQ6 |
| 23 | SA8 | IRQ5 |
| 24 | SA7 | IRQ4 |
| 25 | SA6 | IRQ3 |
| 26 | SA5 | DACK2* |
| 27 | SA4 | TC |
| 28 | SA3 | BALE |
| 29 | SA2 | +5V |
| 30 | SA1 | OSC |
| 31 | SA0 | GND |
| 32 | GND | GND |

| J2/P2 | | |
|-------|----------|-------|
| Pin | Row D | Row C |
| 0 | GND | GND |
| 1 | MEMCS16* | SBHE* |
| 2 | IOCS16* | LA23 |
| 3 | IRQ10 | LA22 |
| 4 | IRQ11 | LA21 |
| 5 | IRQ12 | LA20 |
| 6 | IRQ15 | LA19 |
| 7 | IRQ14 | LA18 |
| 8 | DACK0* | LA17 |
| 9 | DRQ0 | MEMR* |
| 10 | DACK5* | MEMW* |
| 11 | DRQ5 | SD8 |
| 12 | DACK6* | SD9 |
| 13 | DRQ6 | SD10 |
| 14 | DACK7* | SD11 |
| 15 | DRQ7 | SD12 |
| 16 | +5V | SD13 |
| 17 | MASTER* | SD14 |
| 18 | GND | SD15 |
| 19 | GND | KEY |

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